CLAIMS

What is claimed is:

1. A method for overriding a signal during model simulation, said method comprising:

   instantiating an override signal port for delivering an override signal from an instrumentation entity to a signal selection means, wherein said signal selection means selects between said signal and said override signal;

   declaring a signal override during model simulation; and

   in response to said declared signal override, selecting said override signal utilizing said signal selection means.

2. The method of claim 1, wherein said declaring step further comprises instantiating an override enable port for delivering an override enable signal.

3. The method of claim 2, further comprising delivering said override enable signal from said override enable port to said signal selection means.

4. The method of claim 2, further comprising:

   instantiating a latch that stores an override disable bit; and

4   combining said override disable bit with said

5   override enable signal within a logic gate to produce a

6   combined selection signal that controls said signal

7   selection means.